



UNITED STATES PATENT AND TRADEMARK OFFICE

B / IPO
Image

Group Art Unit: 2829
Examiner: J. Hollington

PATENT APPLICATION OF:

Applicant: Mikio OHTAKI)
Appln. No.: 09/904,663 ✓)
Filed: July 16, 2001)
For: METHOD FOR MANUFACTURING)
AND BATCH TESTING)
SEMICONDUCTOR DEVICES)
Atty Ref.: KAN 120D1)

**SUBMISSION OF
REPLACEMENT
DRAWINGS**

December 13, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance dated October 12, 2004, Applicant hereby submits five (5) sheets of replacement Formal Drawings (Figs. 29-33). The formal drawings incorporate the requirements set forth in the Notice of Draftsperson's Patent Drawing Review (PTO-948) dated September 23, 2002.

Respectfully submitted,

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